

2MBI75VA-170-50

IGBT Modules

IGBT MODULE (V series) 1700V / 75A / 2 in one package

■ Features

- High speed switching
- Voltage drive
- Low Inductance module structure

■ Applications

- Inverter for Motor Drive
- AC and DC Servo Drive Amplifier
- Uninterruptible Power Supply
- Industrial machines, such as Welding machines

■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at T_c=25°C unless otherwise specified)

Items	Symbols	Conditions	Maximum ratings	Units	
Collector-Emitter voltage	V _{CEs}		1700	V	
Gate-Emitter voltage	V _{GES}		±20	V	
Collector current	I _c	Continuous	T _c =100°C	75	A
			T _c =25°C	110	
	I _{c pulse}	1ms	150		
	-I _c		75		
Collector power dissipation	P _C	1 device		555	W
				175	
Junction temperature	T _j		175	°C	
Operating junction temperature (under switching conditions)	T _{jop}		150		
Case temperature	T _c		125		
Storage temperature	T _{stg}		-40 ~ 125		
Isolation voltage	V _{iso}	AC : 1min.	4000	VAC	
Screw torque	Mounting (*2)		5.0	N·m	
	Terminals (*3)		5.0		

Note *1: All terminals should be connected together when isolation test will be done.

Note *2: Recommendable Value : 3.0~5.0 N·m (M5 or M6)

Note *3: Recommendable Value : 2.5~5.0 N·m (M5)

● Electrical characteristics (at T_j = 25°C unless otherwise specified)

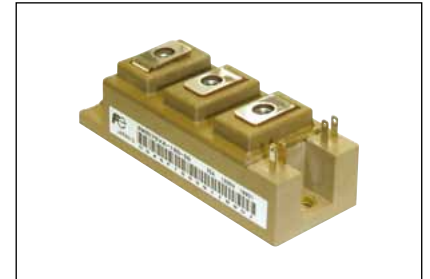
Items	Symbols	Conditions	Characteristics			Units	
			min.	typ.	max.		
Zero gate voltage collector current	I _{CEs}	V _{GE} = 0V, V _{CE} = 1700V	-	-	1.0	mA	
Gate-Emitter leakage current	I _{GES}	V _{CE} = 0V, V _{GE} = ±20V	-	-	200	nA	
Gate-Emitter threshold voltage	V _{GE(th)}	V _{CE} = 20V, I _c = 75mA	6.0	6.5	7.0	V	
Collector-Emitter saturation voltage	V _{CE(sat)} (terminal)	V _{GE} = 15V I _c = 75A	T _j =25°C	-	2.10	2.55	V
			T _j =125°C	-	2.55	-	
			T _j =150°C	-	2.60	-	
	T _j =25°C		-	2.00	2.45		
	T _j =125°C		-	2.40	-		
	T _j =150°C		-	2.45	-		
Internal gate resistance	R _{G(int)}	-	-	10	-	Ω	
	C _{ies}	V _{CE} = 10V, V _{GE} = 0V, f = 1MHz	-	8.2	-	nF	
Turn-on time	t _{on}	V _{CC} = 900V, I _c = 75A V _{GE} = ±15V, R _{g_on} =R _{g_off} = 22Ω T _j =150°C, L _s = 30nH	-	1250	-	nsec	
	t _r		-	550	-		
	t _(l)		-	70	-		
Turn-off time	t _{off}		-	1300	-	nsec	
	t _r		-	150	-		
Forward on voltage	V _F (terminal)	V _{GE} = 0V I _F = 75A	T _j =25°C	-	1.85	2.30	V
			T _j =125°C	-	2.10	-	
			T _j =150°C	-	2.10	-	
	T _j =25°C		-	1.80	2.25		
	T _j =125°C		-	2.05	-		
	T _j =150°C		-	2.05	-		
Reverse recovery time	t _{rr}	I _F = 75A	-	140	-	nsec	

● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance(1device)	R _{th(j-c)}	IGBT	-	-	0.27	°C/W
		FWD	-	-	0.50	
Contact thermal resistance (1device) (*4)	R _{th(c-f)}	with Thermal Compound	-	0.050	-	

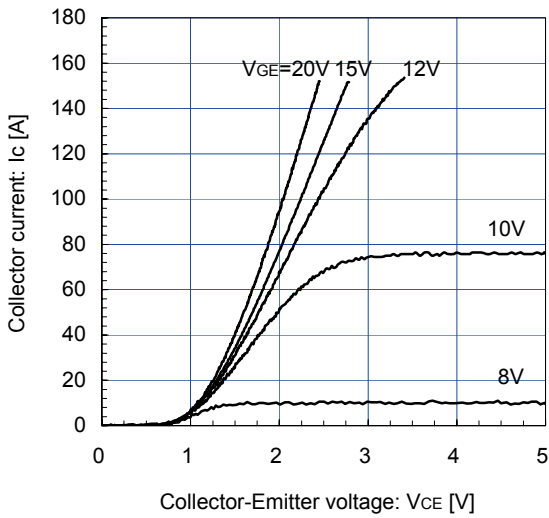
Note *4: This is the value which is defined mounting on the additional cooling fin with thermal compound.

Package No. : M263

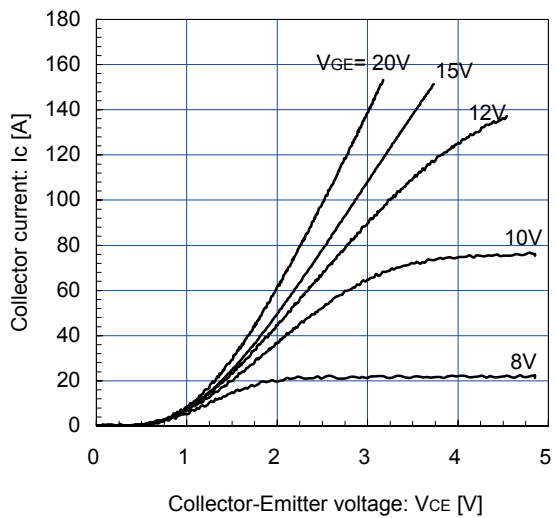


■ Characteristics (Representative)

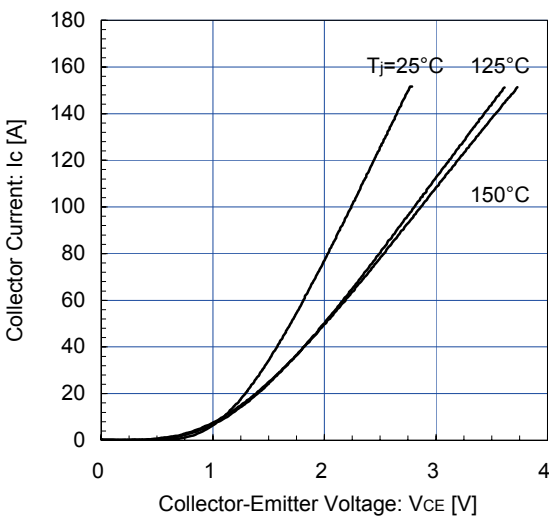
Collector current vs. Collector-Emitter voltage (typ.)
T_j = 25°C / chip



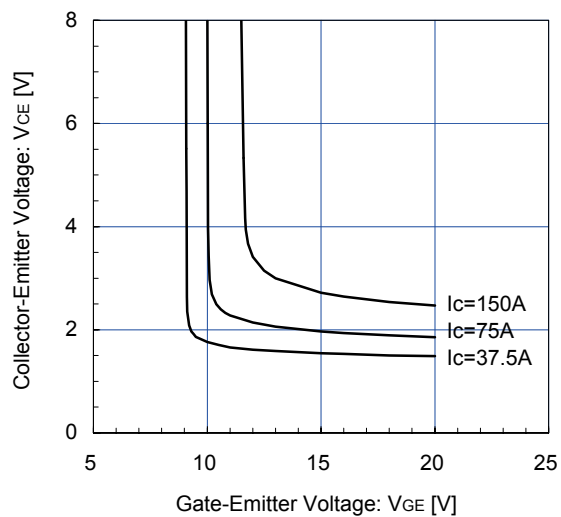
Collector current vs. Collector-Emitter voltage (typ.)
T_j = 150°C / chip



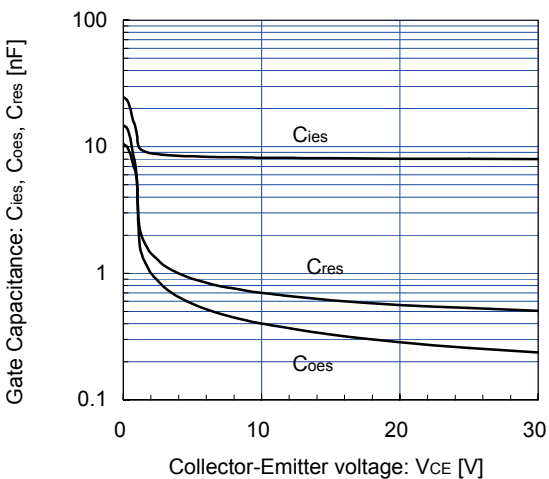
Collector current vs. Collector-Emitter voltage (typ.)
V_{GE} = 15V / chip



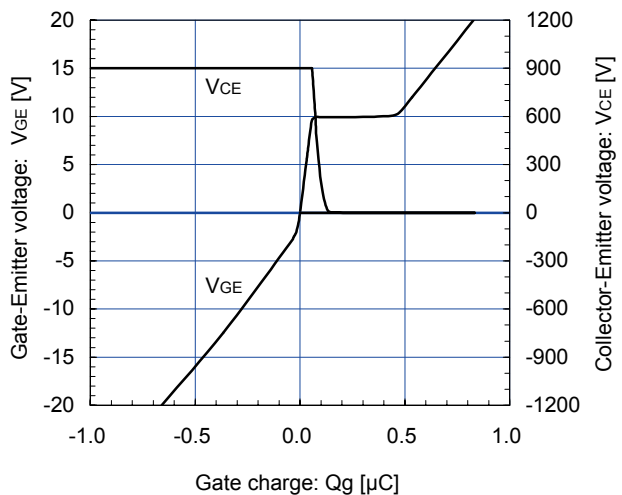
Collector-Emitter voltage vs. Gate-Emitter voltage
T_j = 25°C / chip



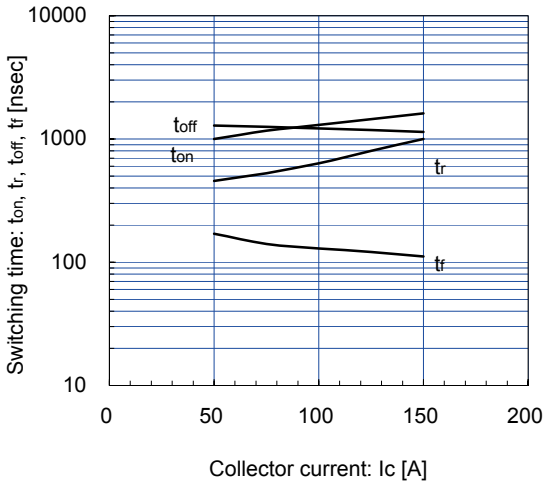
Gate Capacitance vs. Collector-Emitter Voltage
V_{GE} = 0V, f = 1MHz, T_j = 25°C



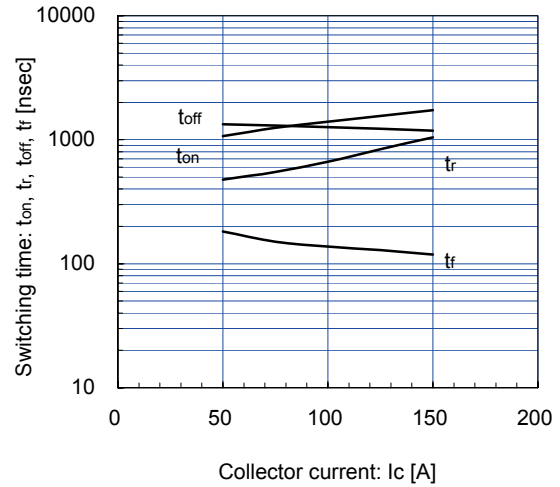
Dynamic Gate Charge (typ.)
V_{CC} = 900V, I_C = 75A, T_j = 25°C



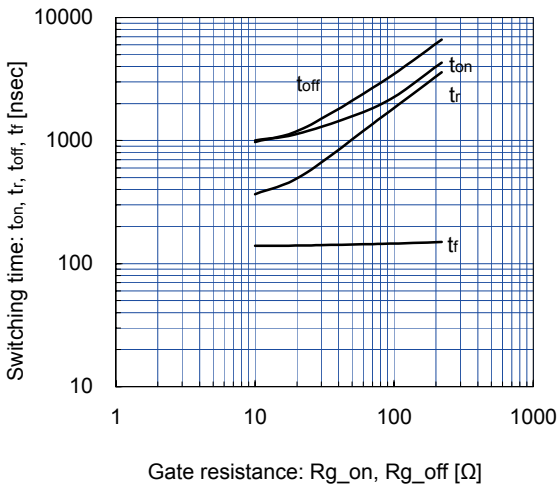
Switching time vs. Collector current (typ.)
 $V_{CC}=900V, V_{GE}=\pm 15V, R_{g_on}=R_{g_off}=22\Omega, T_j=125^\circ C$



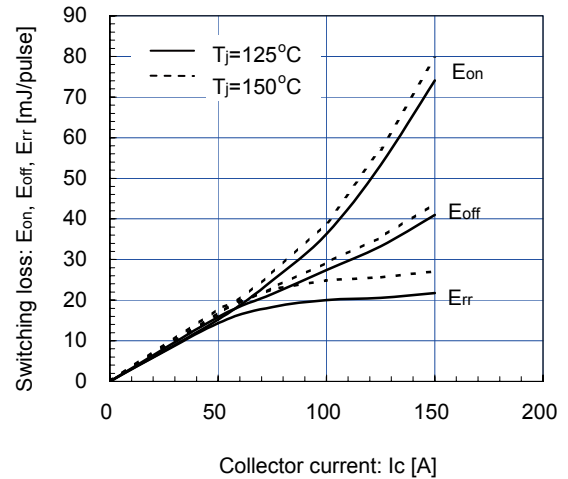
Switching time vs. Collector current (typ.)
 $V_{CC}=900V, V_{GE}=\pm 15V, R_{g_on}=R_{g_off}=22\Omega, T_j=150^\circ C$



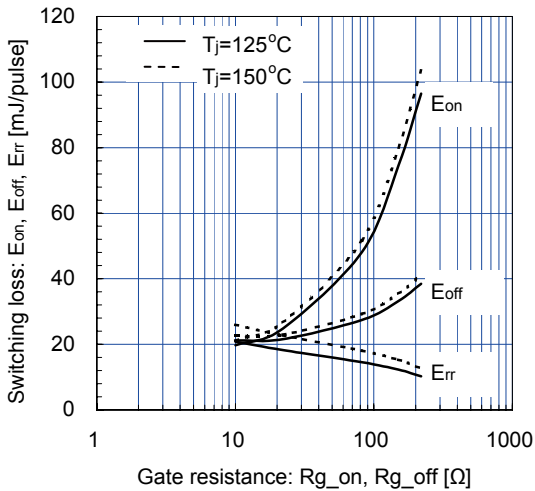
Switching time vs. Gate resistance (typ.)
 $V_{CC}=900V, I_c=75A, V_{GE}=\pm 15V, T_j=125^\circ C$



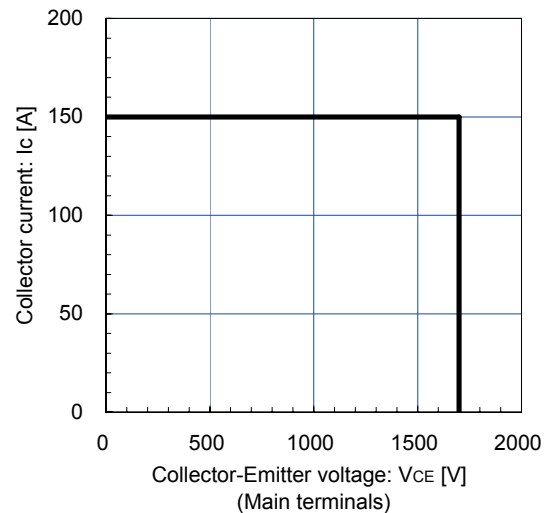
Switching loss vs. Collector current (typ.)
 $V_{CC}=900V, V_{GE}=\pm 15V, R_{g_on}=R_{g_off}=22\Omega$



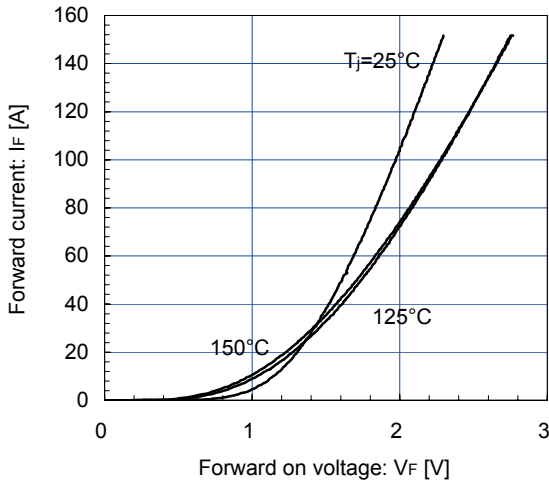
Switching loss vs. Gate resistance (typ.)
 $V_{CC}=900V, I_c=75A, V_{GE}=\pm 15V, T_j=125, 150^\circ C$



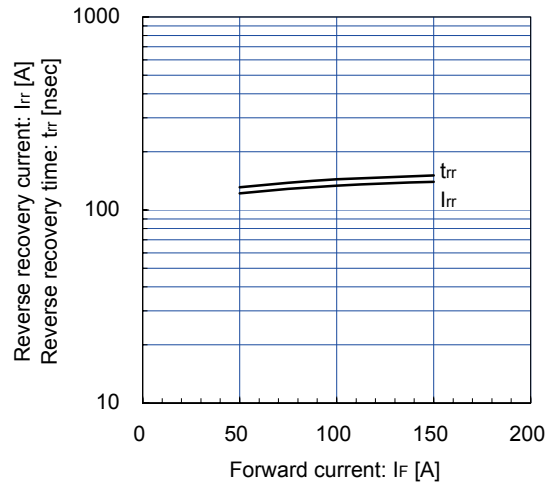
Reverse bias safe operating area (max.)
 $+V_{GE}=15V, -V_{GE}=15V, R_{g_off}=22\Omega, T_j=150^\circ C$



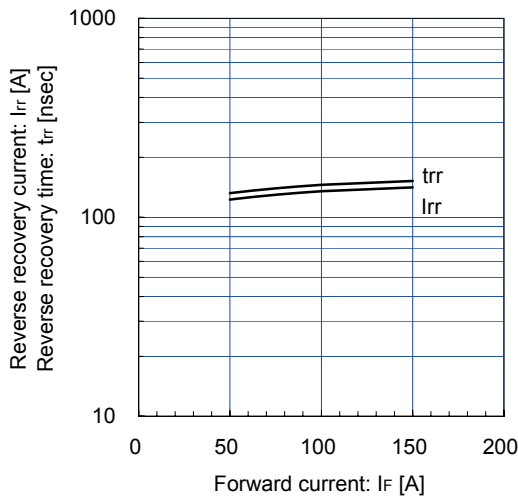
Forward Current vs. Forward Voltage (typ.)
chip



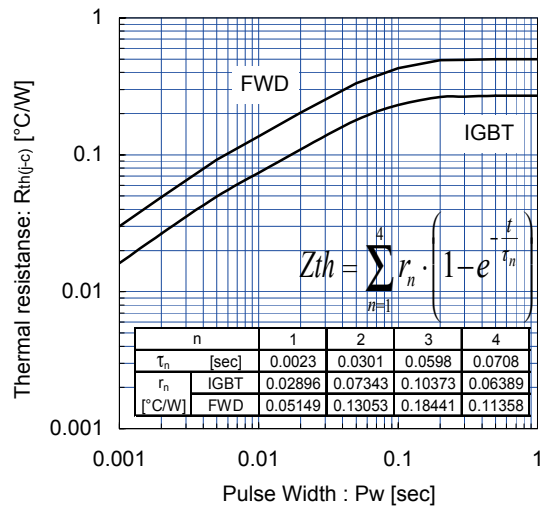
Reverse Recovery Characteristics (typ.)
V_{CC}=900V, V_{GE}=±15V, R_{g_on}=22Ω, T_j=125°C



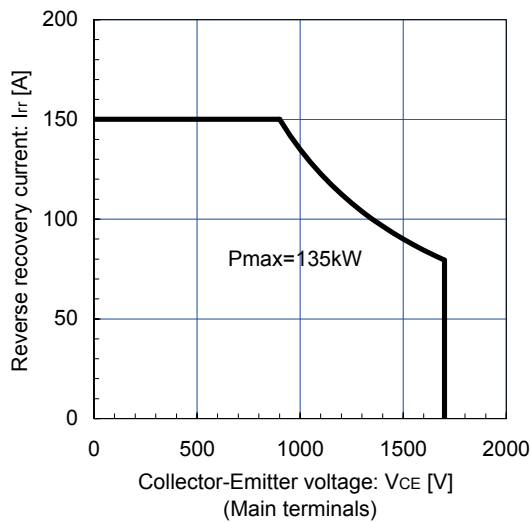
Reverse Recovery Characteristics (typ.)
V_{CC}=900V, V_{GE}=±15V, R_{g_on}=22Ω, T_j=150°C



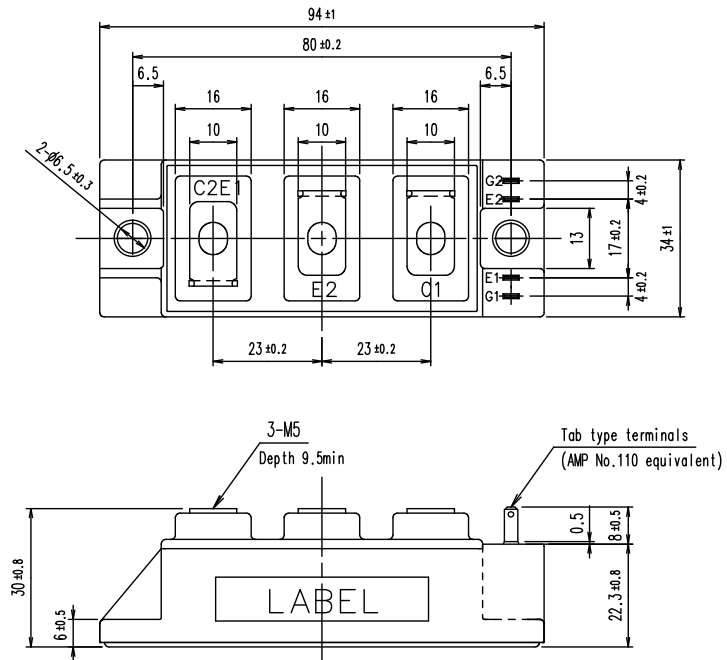
Transient Thermal Resistance (max.)



FWD safe operating area (max.)
T_j=150°C

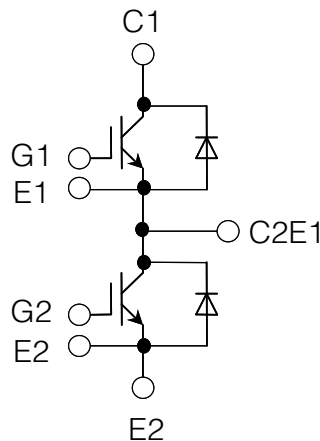


■ Outline Drawings, mm



Weight: 180g (typ.)

■ Equivalent Circuit Schematic



WARNING

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