

2MBI1200VG-120P

IGBT Modules

IGBT MODULE (V series) 1200V / 1200A / 2 in one package

■ Features

- High speed switching
- Voltage drive
- Low Inductance module structure

■ Applications

- Inverter for Motor Drive
- AC and DC Servo Drive Amplifier
- Uninterruptible Power Supply
- Industrial machines, such as Welding machines



■ Maximum Ratings and Characteristics

● Absolute Maximum Ratings (at Tc=25°C unless otherwise specified)

Items	Symbols	Conditions	Maximum Ratings	Units
Collector-Emitter voltage	V_{CES}		1200	V
Gate-Emitter voltage	V_{GES}		±20	V
Collector current	I_c	Continuous	Tc=25°C 1600	A
	I_{cp}	1ms	Tc=100°C 1200	
	$-I_c$		Tc=100°C 2400	
	$-I_{cpulse}$	1ms	2400	
Collector Power Dissipation	P_c	1 device	6810	W
Junction temperature	T_j		175	°C
Operating junction temperature(under switching conditions)	T_{jop}		150	
Storage temperature	T_{stg}		-40 ~ +150	
Isolation voltage between terminal and copper base *1	V_{iso}	AC : 1min.	4000	VAC
	Mounting	M6	5.75	N m
Screw Torque *2	Main Terminals	M8	10	
	Sense Terminals	M4	2.5	

(*1) All terminals should be connected together when isolation test will be done.

(*2) Recommendable Value :Mounting 4.25~5.75 Nm (M6) , Main Terminals 8~10 Nm (M8) , Sense Terminals 1.7~2.5 Nm (M4)

● Electrical characteristics (at $T_j = 25^\circ\text{C}$ unless otherwise specified)

Items	Symbols	Conditions	Characteristics			Units	
			min.	typ.	max.		
Zero gate voltage Collector current	I_{CES}	$V_{GE} = 0V$, $V_{CE} = 1200V$	-	-	1.0	mA	
Gate-Emitter leakage current	I_{GES}	$V_{CE} = 0V$, $V_{GE} = \pm 20V$	-	-	1600	nA	
Gate-Emitter threshold voltage	$V_{GE(th)}$	$V_{CE} = 20V$, $I_c = 1200mA$	6.0	6.5	7.0	V	
Collector-Emitter saturation voltage	$V_{CE(sat)}$ (main terminal)	$V_{GE} = 15V$ $I_c = 1200A$	$T_j = 25^\circ\text{C}$	-	2.02	2.31	V
			$T_j = 125^\circ\text{C}$	-	2.32	-	
			$T_j = 150^\circ\text{C}$	-	2.42	-	
	$V_{CE(sat)}$ (chip)		$T_j = 25^\circ\text{C}$	-	1.70	1.95	
			$T_j = 125^\circ\text{C}$	-	2.00	-	
$T_j = 150^\circ\text{C}$	-	2.10	-				
Internal gate resistance	I_{ntRg}	-	-	1.88	-	Ω	
Input capacitance	C_{ies}	$V_{CE} = 10V$, $V_{GE} = 0V$, $f = 1MHz$	-	104	-	nF	
Turn-on	t_{on}	$V_{CC} = 600V$ $I_c = 1200A$ $L_m = 75nH$	-	2.55	-	μs	
	t_r	$V_{GE} = \pm 15V$, $T_j = 125^\circ\text{C}$	-	0.82	-		
Turn-off	t_{off}	$R_{gon} = 2.4 \Omega$ $R_{goff} = 0.22 \Omega$	-	1.67	-		
	t_f		-	0.16	-		
Forward on voltage	V_F (main terminal)	$V_{GE} = 0V$ $I_F = 1200A$	$T_j = 25^\circ\text{C}$	-	2.02	2.31	V
			$T_j = 125^\circ\text{C}$	-	2.17	-	
			$T_j = 150^\circ\text{C}$	-	2.12	-	
	V_F (chip)		$T_j = 25^\circ\text{C}$	-	1.70	1.95	
			$T_j = 125^\circ\text{C}$	-	1.85	-	
$T_j = 150^\circ\text{C}$	-	1.80	-				
Reverse recovery	R_{tr}	$I_F = 1200A$, $T_j = 125^\circ\text{C}$	-	0.36	-	μs	
Lead resistance, terminal-chip	R_{lead}	-	-	0.268	-	m Ω	

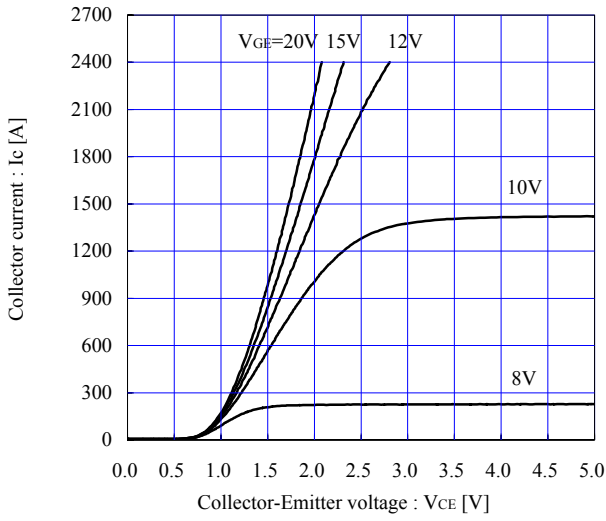
● Thermal resistance characteristics

Items	Symbols	Conditions	Characteristics			Units
			min.	typ.	max.	
Thermal resistance	$R_{th(j-c)}$	IGBT	-	-	0.0220	$^\circ\text{C/W}$
		FWD	-	-	0.0360	
Contact thermal resistance	$R_{th(c-f)}$	with Thermal Compound(*)	-	0.0060	-	

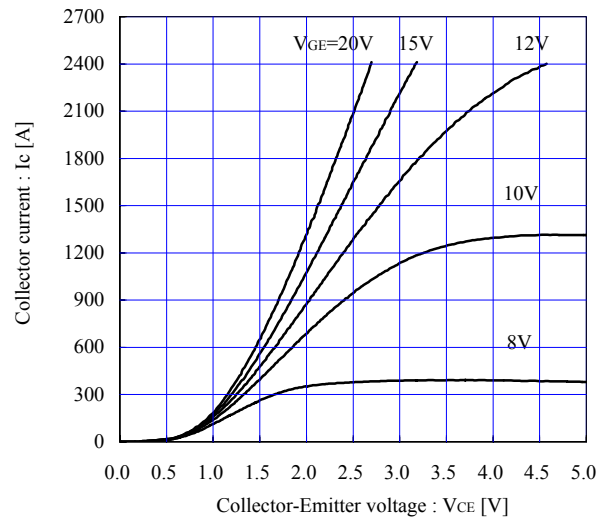
*This is the value which is defined mounting on the additional cooling fin with thermal compound.

■ Characteristics (Representative)

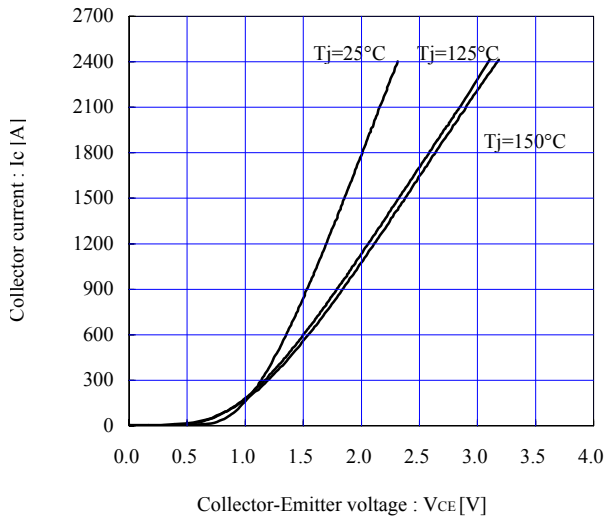
Collector current vs. Collector-Emittter voltage (typ.)
T_j=25°C, chip



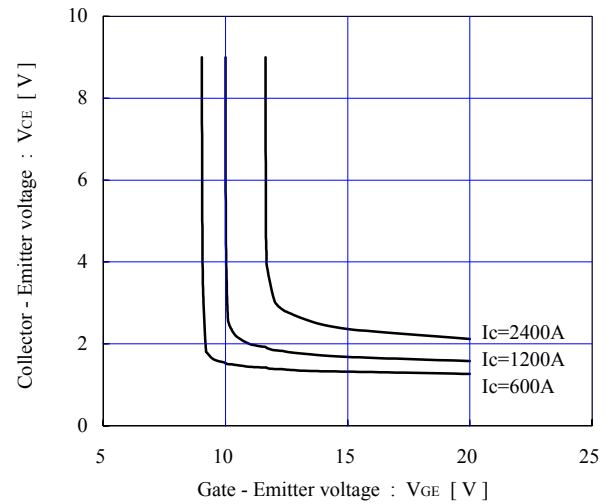
Collector current vs. Collector-Emittter voltage (typ.)
T_j= 150°C, chip



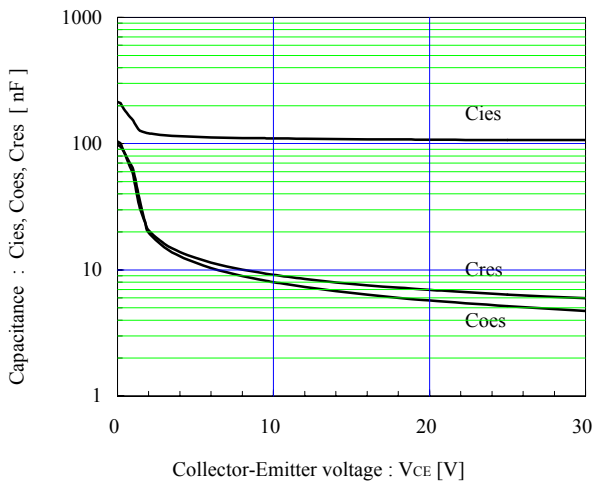
Collector-Emittter voltage vs. Gate-Emittter voltage (typ.)
V_{GE}=+15V, chip



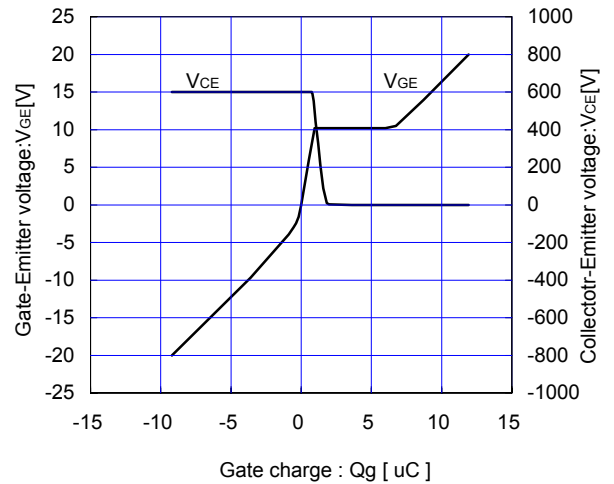
Collector-Emittter voltage vs. Gate-Emittter voltage (typ.)
T_j=25°C, chip



Capacitance vs. Collector-Emittter voltage (typ.)
V_{GE}=0V, f= 1MHz, T_j= 25°C

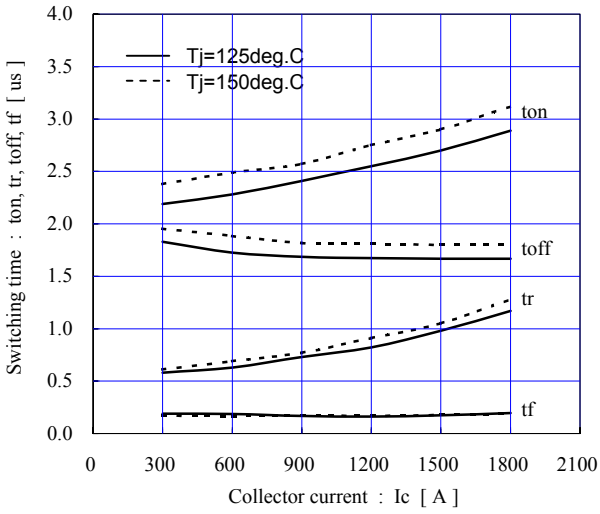


Dynamic Gate charge (typ.)
T_j= 25°C



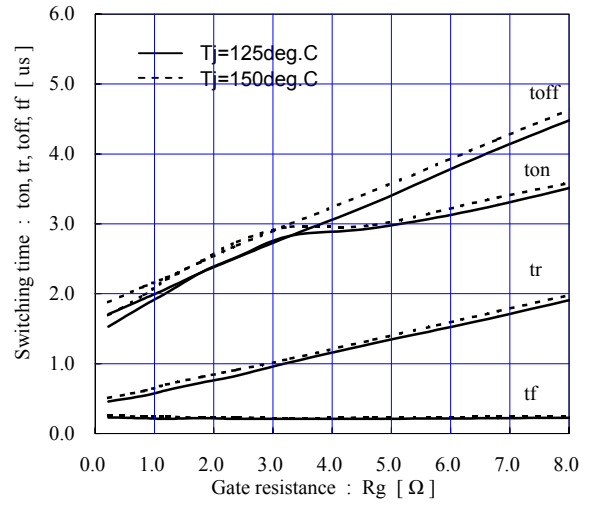
Switching time vs. Collector current (typ.)

$V_{cc}=600V, V_{GE}=\pm 15V, R_{gon}=2.4 \Omega, R_{goff}=0.22 \Omega$



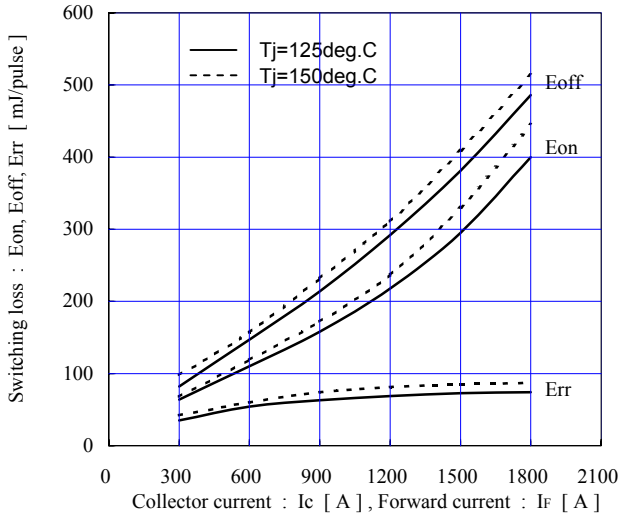
Switching time vs. Gate resistance (typ.)

$V_{cc}=600V, I_c=1200A, V_{GE}=\pm 15V$



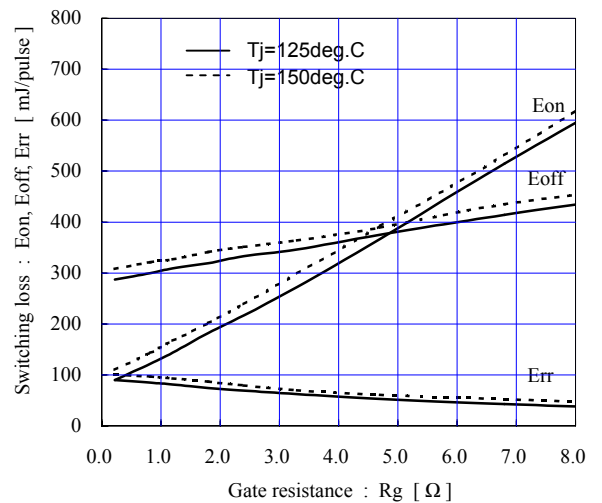
Switching loss vs. Collector current (typ.)

$V_{cc}=600V, V_{GE}=\pm 15V, R_{gon}=2.4 \Omega, R_{goff}=0.22 \Omega$



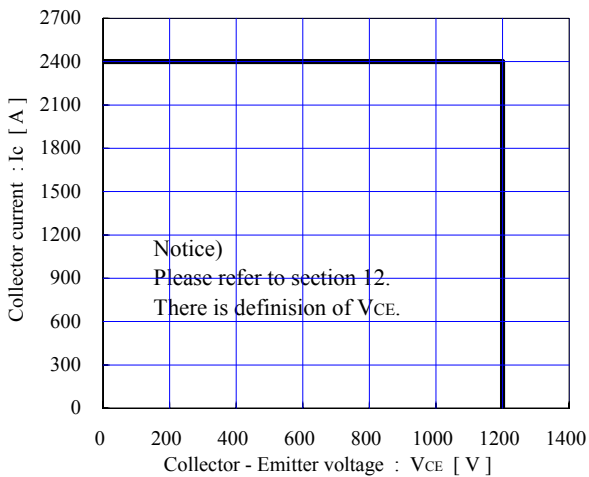
Switching loss vs. Gate resistance (typ.)

$V_{cc}=600V, I_c=1200A, V_{GE}=\pm 15V$

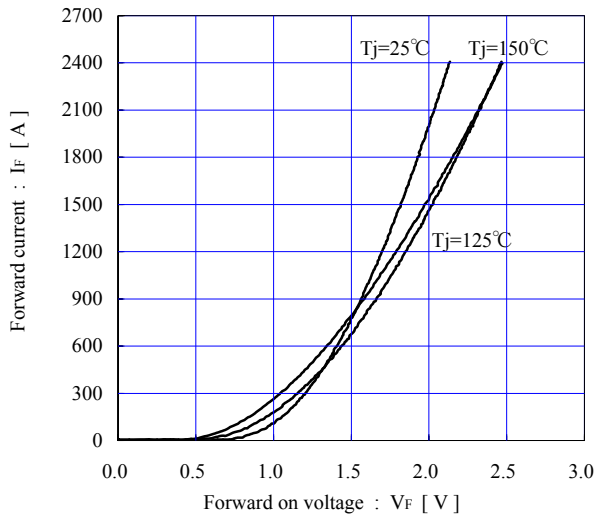


Reverse bias safe operating area (max.)

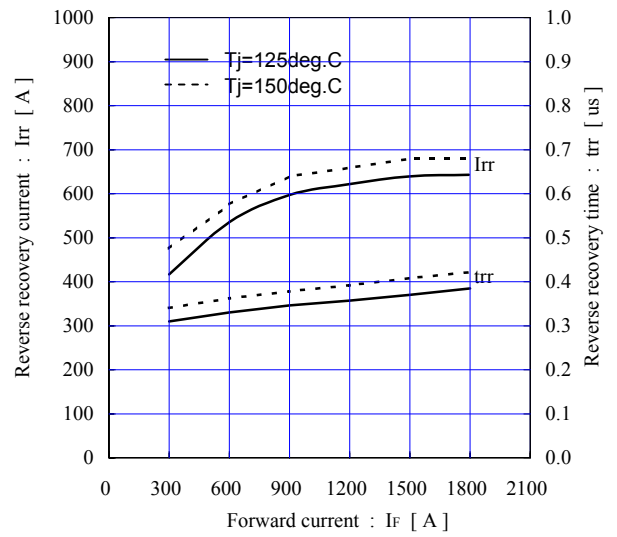
$\pm V_{GE}=15V, T_j = 150^\circ C$



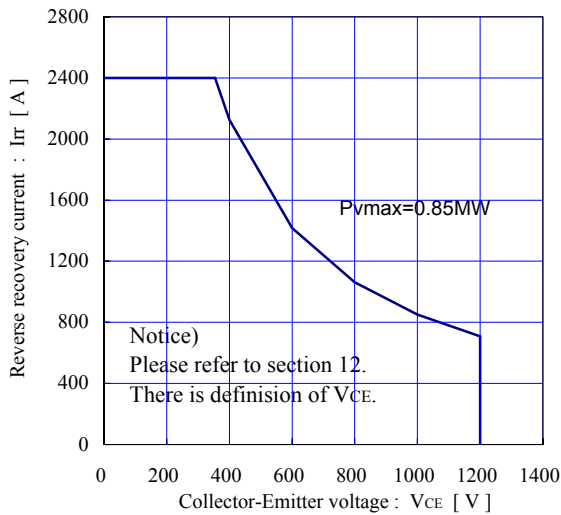
Forward current vs. Forward on voltage (typ.)
chip



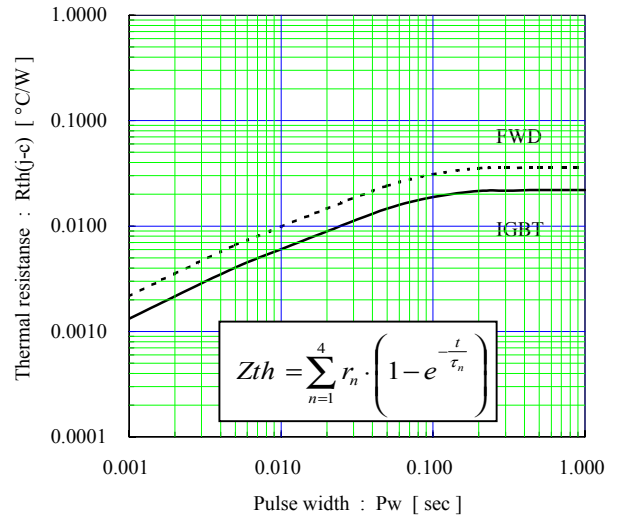
Reverse recovery characteristics (typ.)
V_{CC}=600V, V_{GE}=±15V, R_{gon}=2.4 Ω



FWD safe operating area (max.)
T_j=150°C

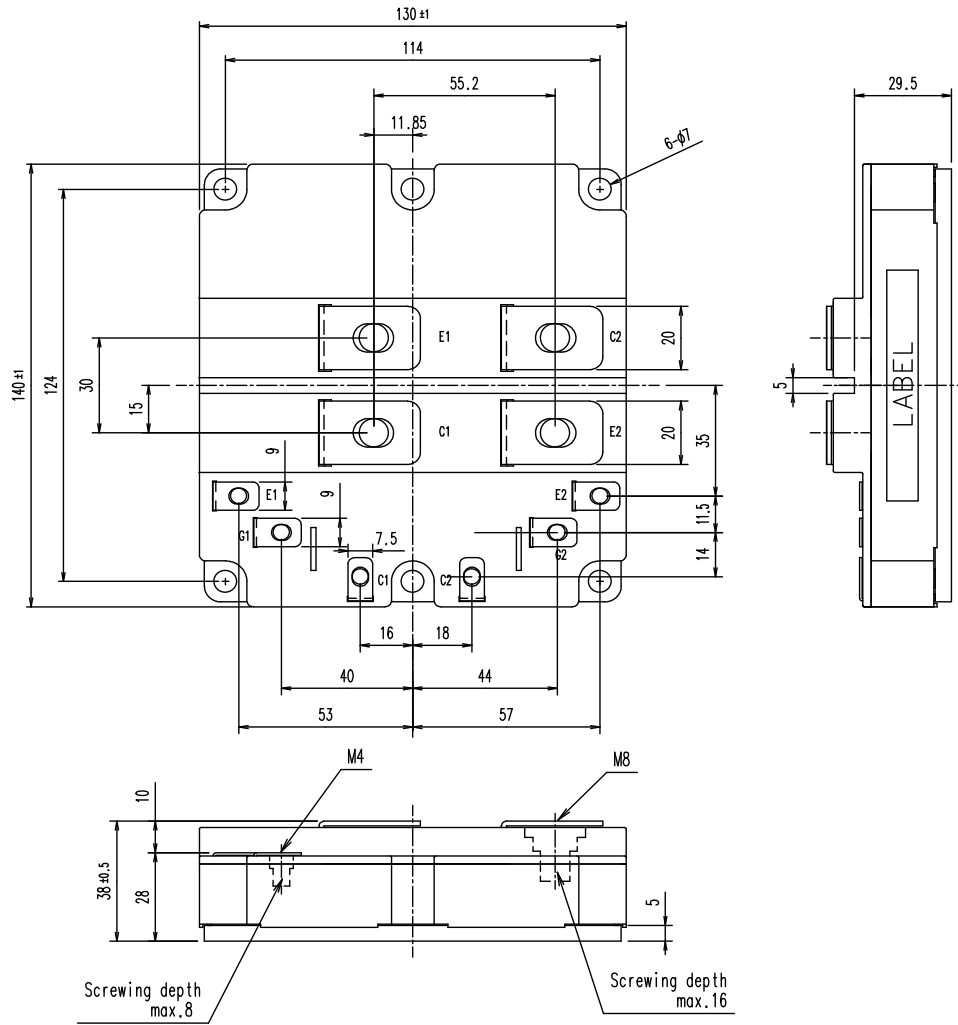


Transient thermal resistance (max.)

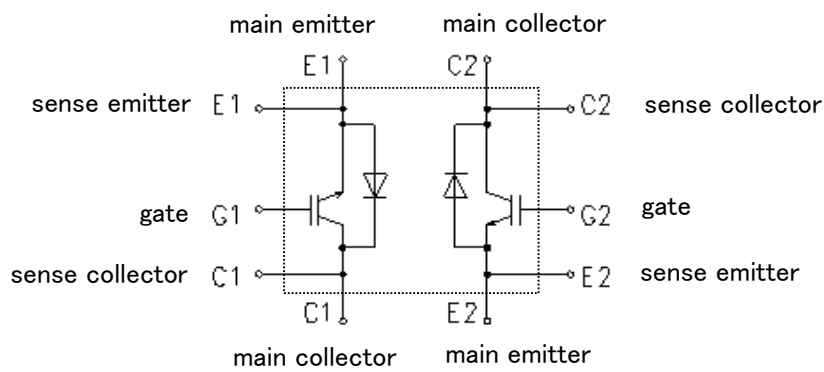


	IGBT	FWD
r1	0.00242	0.00386
r2	0.00837	0.01056
r3	0.00515	0.01276
r4	0.00606	0.00882
τ1	0.0023	0.0023
τ2	0.0349	0.0310
τ3	0.0679	0.0623
τ4	0.0681	0.0682

■ Outline Drawings, mm



■ Equivalent Circuit Schematic



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